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6229506.pn.	2

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DATE: Monday, November 22, 2004 [Printable Copy](#) [Create Case](#)

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L6</u>	6229506.pn.	2	<u>L6</u>
<u>L5</u>	6229508.pn.	2	<u>L5</u>
<u>L4</u>	L3 and (tim\$3 adj period)	8	<u>L4</u>
<u>L3</u>	L2 and current	18	<u>L3</u>
<u>L2</u>	L1 and (threshold adj voltage) (active adj matrix) and display and pixels and array and (second adj transistor)	18	<u>L2</u>
<u>L1</u>	and (light adj emitting adj diode) and ((stor\$3 or holding) adj capacitor) and compensat\$3	20	<u>L1</u>

END OF SEARCH HISTORY